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| **JOB DESCRIPTION** | | | |
| **I. JOB DETAILS** | | | |
| **Job Title** | Design Engineer | | |
| **Function** |  | **No. of Openings** | 2 |
| **Location** | Bangalore | | |
| **About Company/ Client:**  **Schneider Electric SE** is a French [multinational company](https://en.wikipedia.org/wiki/Multinational_corporation) that specializes in digital automation and energy management. It addresses homes, buildings, data centres, infrastructure and industries, by combining energy technologies, real-time automation, software, and services.  Schneider Electric is a [*Fortune* Global 500](https://en.wikipedia.org/wiki/Fortune_Global_500) company, publicly traded on the Euronext Exchange, and is a component of the [Euro Stoxx 50](https://en.wikipedia.org/wiki/Euro_Stoxx_50) [stock market index](https://en.wikipedia.org/wiki/Stock_market_index). In [fiscal year](https://en.wikipedia.org/wiki/Fiscal_year) 2022, the company posted revenues of €34.2 billion.  Schneider Electric is the [parent company](https://en.wikipedia.org/wiki/Parent_company) of [Square D](https://en.wikipedia.org/wiki/Square_D), [APC](https://en.wikipedia.org/wiki/APC_by_Schneider_Electric), and others. It is also a research company. | | | |
| **II. KEY RESPONSIBILITIES** | | | |
| 1. Experience with PCB design package PADS Mentor Graphics and Cadence Design Entry HDL(Concept HDL) is a must 2. Schematic symbol creation, drafting in Pads logic and Concept HDL software 3. Component footprint/library creation as per the industrial standards 4. PCB Layout designing for quick turnaround proto type and volume manufacturing 5. Provide support for Hardware designer to draft schematic and part creation 6. Strong experience in designing power electronics PCB’s for electronic product development 7. Knowledge and implementation of creep age and clearance requirements in PCB’s 8. Interacting with mechanical and electrical engineers and understand mechanical packaging in CAD model 9. Experience in PCB design process ,Gerber Generation and manufacturing documentation | | | |
| **Qualifications** | BE/Diploma | | |
| **Experience** | Minimum 6 Years’ Experience  Software Experience  Cadence- Concept HDL and Allegro PCB editor  Mentor- Pads Logic and Layout | | |